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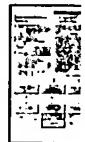
Title: **AU0723724B2: MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PROCESSOR**

Derwent Title: Multiple network protocol encoder-decoder - has network protocol layer, data handler, operating system state machine and memory manager, and protocol handler which parses and strips header information from each packet immediately [\[Derwent Record\]](#)

Country: **AU** Australia

Kind: **B2** Patent (APP. advertised accepted)

Inventor: **JOHN SHIGETO MINAMI;**
RYO KOYAMA;
MICHAEL WARD JOHNSON;
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Published / Filed: **2000-09-07 / 1997-09-26**

Application Number: **AU1997000045952**

IPC Code: **H04J 3/16; H04J 3/22;**

ECLA Code: **None**

Priority Number: **1996-10-31 US1996000742085**
1997-09-26 WO1997000017257

INPADOC Legal Status: **None** Get Now: [Family Legal Status Report](#)

Designated Country: **AL AM AP AZ BA BB BG BR BY CA CU CZ EE GE HU ID IL IS JP KG KP**
KR KZ LC LK LR LT LV MD MG MK MN MX DE FR GB IT

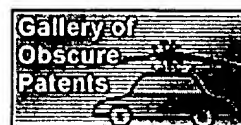
Family:

PDF	Publication	Pub. Date	Filed	Title
	WO9819412A1	1998-05-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
	US6034963	2000-03-07	1996-10-31	Multiple network protocol encoder/decc data processor
<input checked="" type="checkbox"/>	JP2001503577T2	2001-03-13	1997-09-26	
	EP0935855A4	2000-05-17	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
	EP0935855A1	1999-08-18	1997-09-26	MULTIPLE NETWORK PROTOCOL

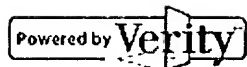
				ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	CN1237295A	1999-12-01	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	CA2265692C	2001-08-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
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<input checked="" type="checkbox"/>	AU4595297A1	1998-05-22	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
<input checked="" type="checkbox"/>	AU0723724B2	2000-09-07	1997-09-26	MULTIPLE NETWORK PROTOCOL ENCODER/DECODER AND DATA PR
10 family members shown above				

Other Abstract
Info:

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